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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named

Applicant: Kyung Joon Han et al.

Docket No.: 11188.00

Title:

METHOD AND APPARATUS FOR MULTIPLE BYTE OR PAGE MODE
PROGRAMMING AND READING AND FOR ERASING OF A FLASH
MEMORY ARRAY

Serial No.

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Name: Jane Lalis

Commissioner for Patents
Washington, D.C. 20231

FILING OF INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)

This Information Disclosure Statement is being filed concurrently with the above-referenced patent application. In accordance with 37 CFR § 1.97(b) to the present knowledge of the undersigned, the accompanying Information Disclosure Citation and the information contained herein are being filed within any one of the following time periods: within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR § 1.491 in an international application; before the mailing of a first Office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR § 1.114. Pursuant to 37 CFR § 1.97(b), no fee is required.

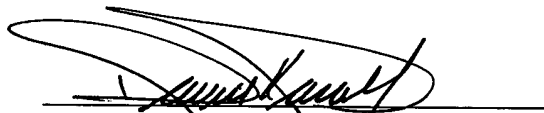


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Respectfully submitted,

Date: _____

Nov. 8, 2001



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